

FIG. 1A

10

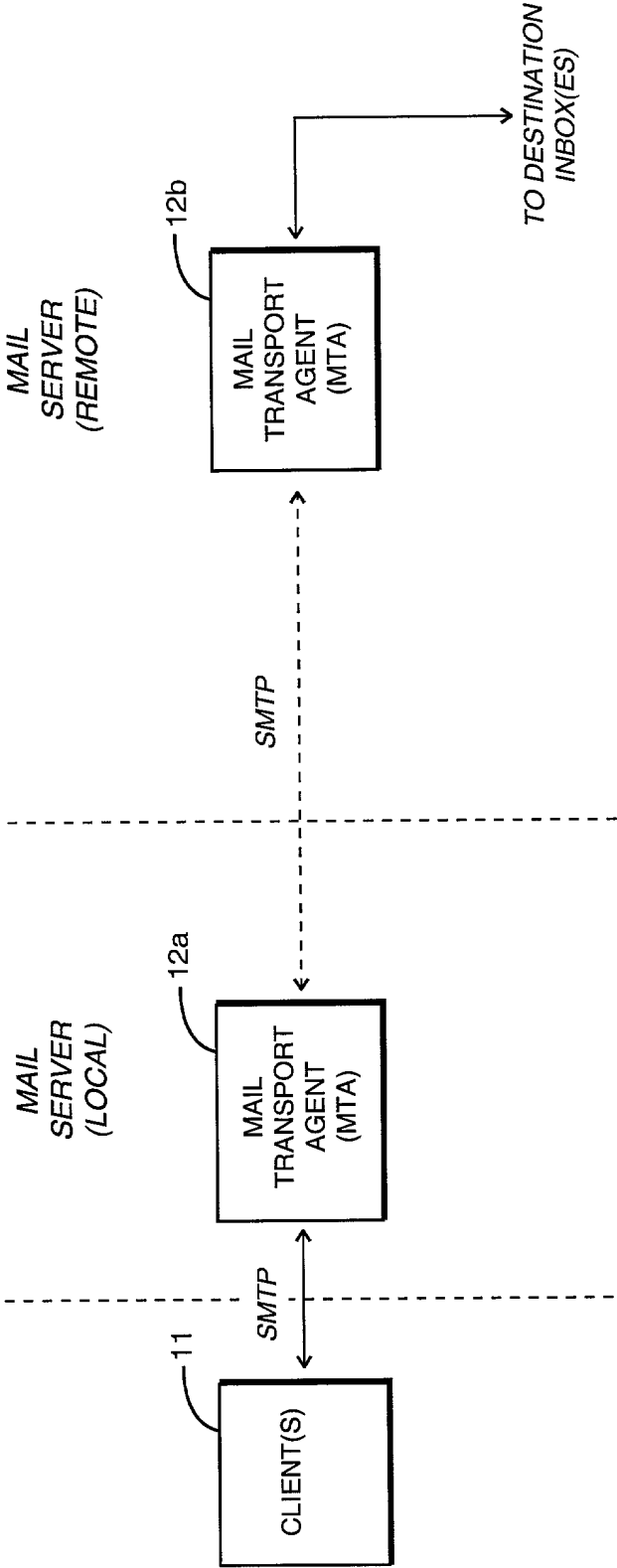
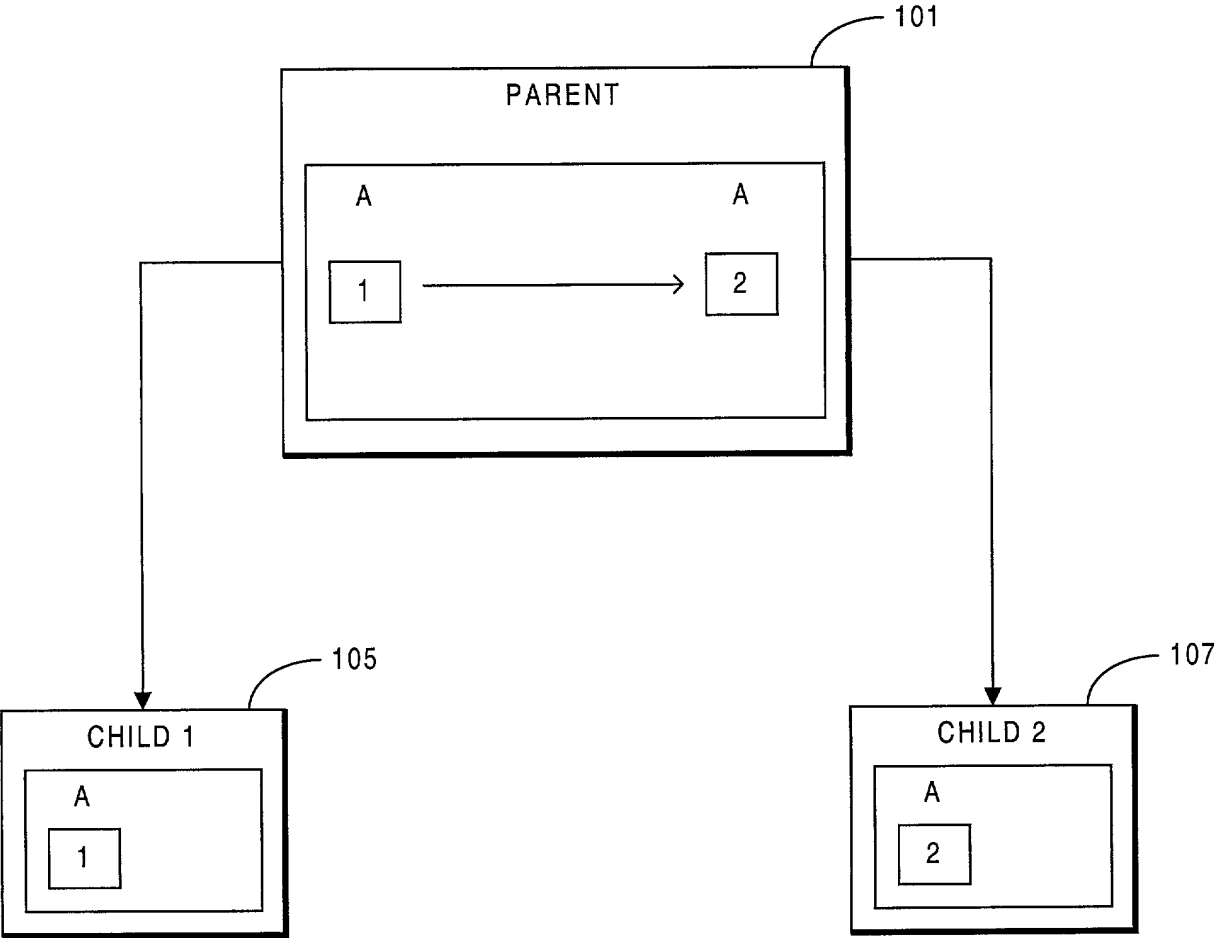


FIG. 1A
(PRIOR ART)



T0: VARIABLE A=1
T1: FORK CHILD 1
T2: PARENT CHANGES VALUE OF VARIABLE A TO 2
T3: FORK CHILD 2

FIG. 1B
(PRIOR ART)

FIG. 200 is a block diagram of a computer system 200.

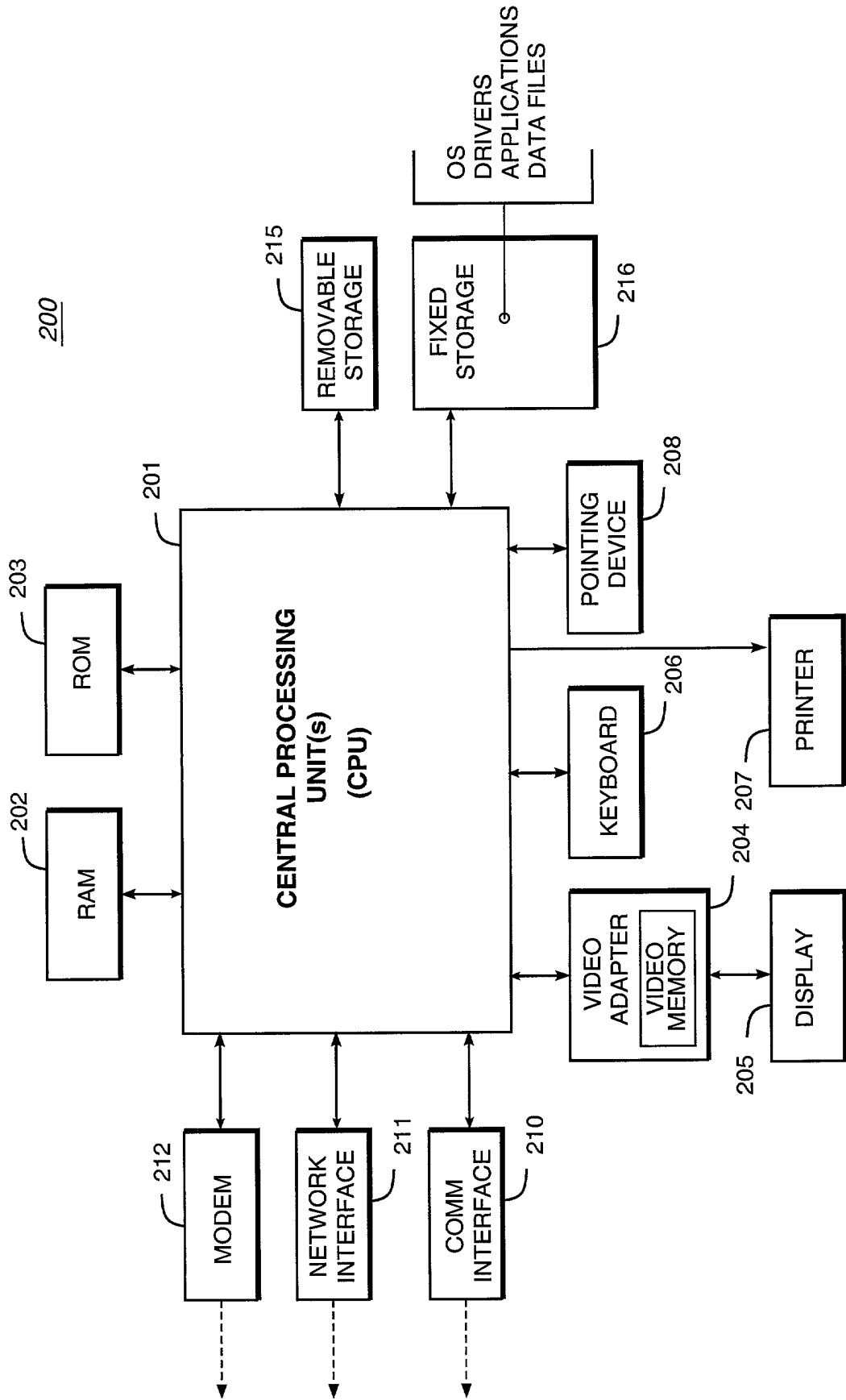


FIG. 2
(PRIOR ART)

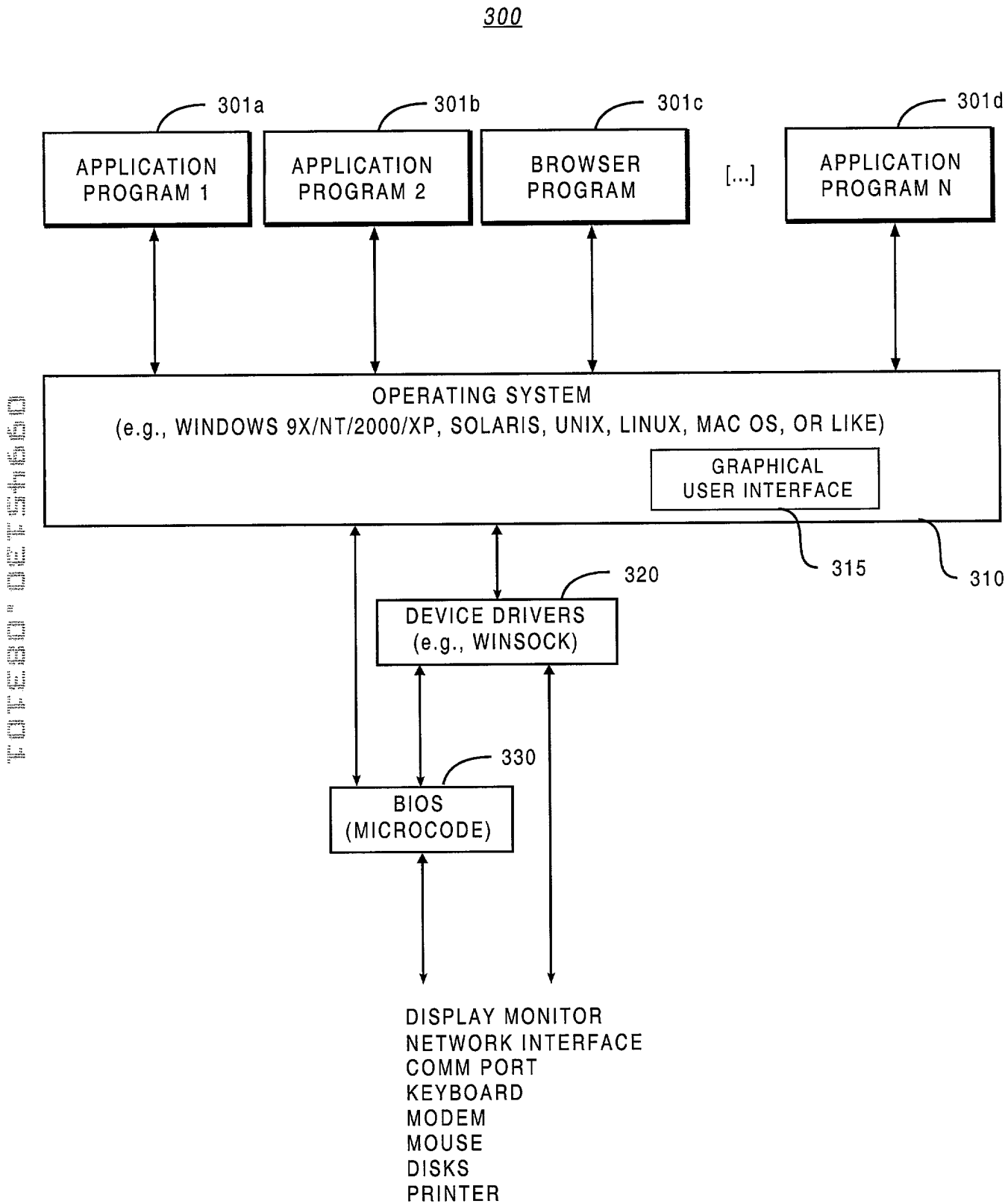


FIG. 3

FIG. 4

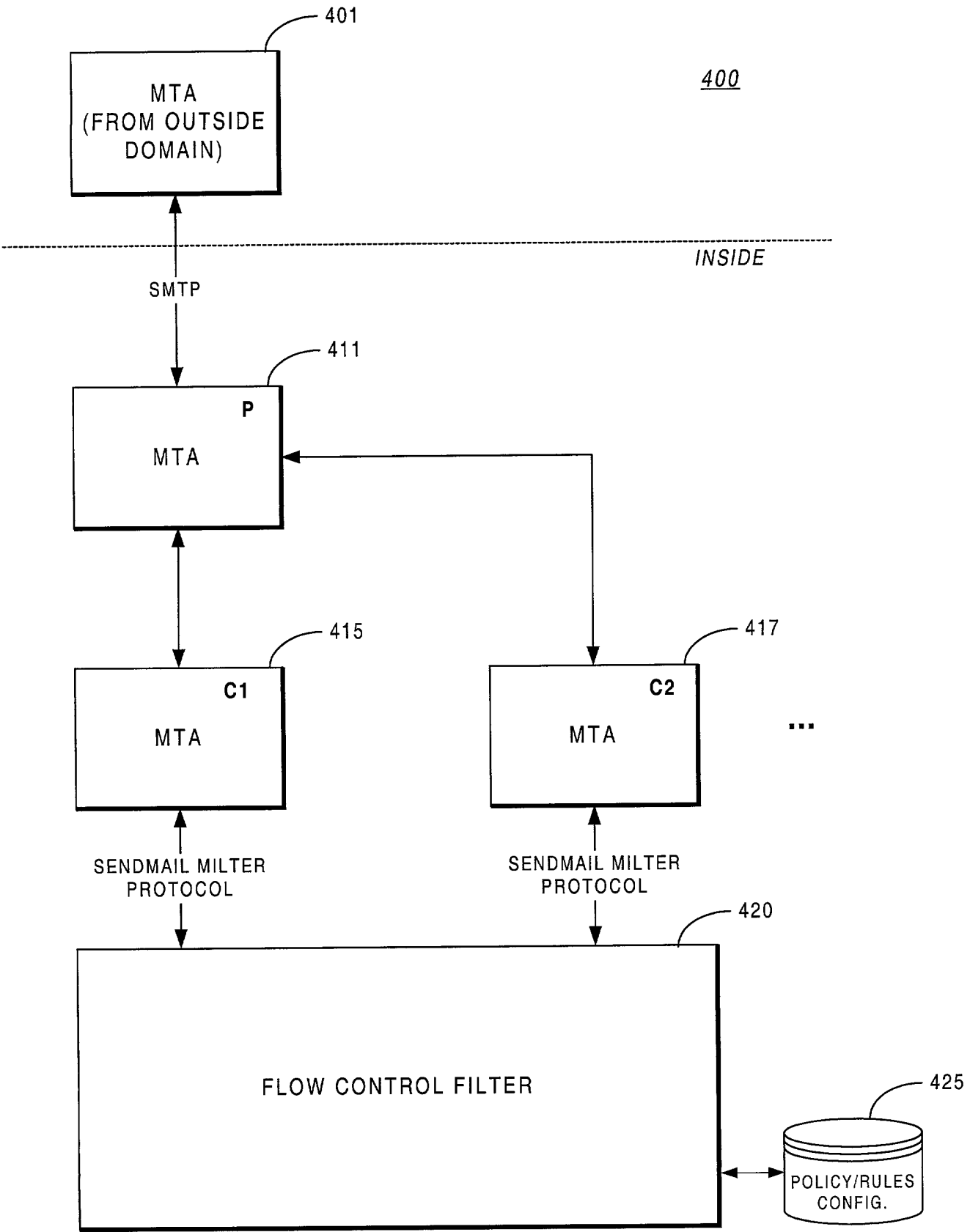


FIG. 4

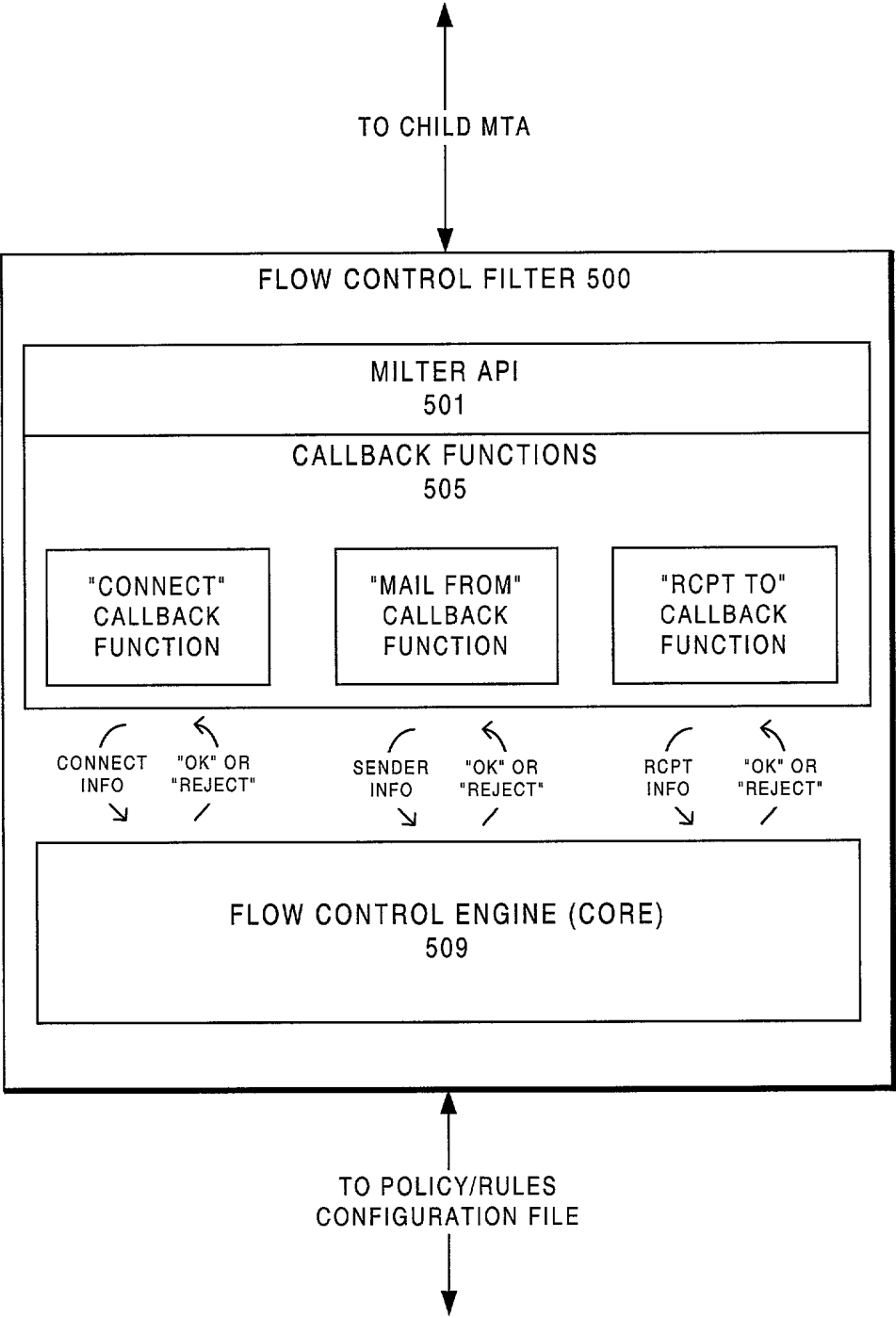


FIG. 5

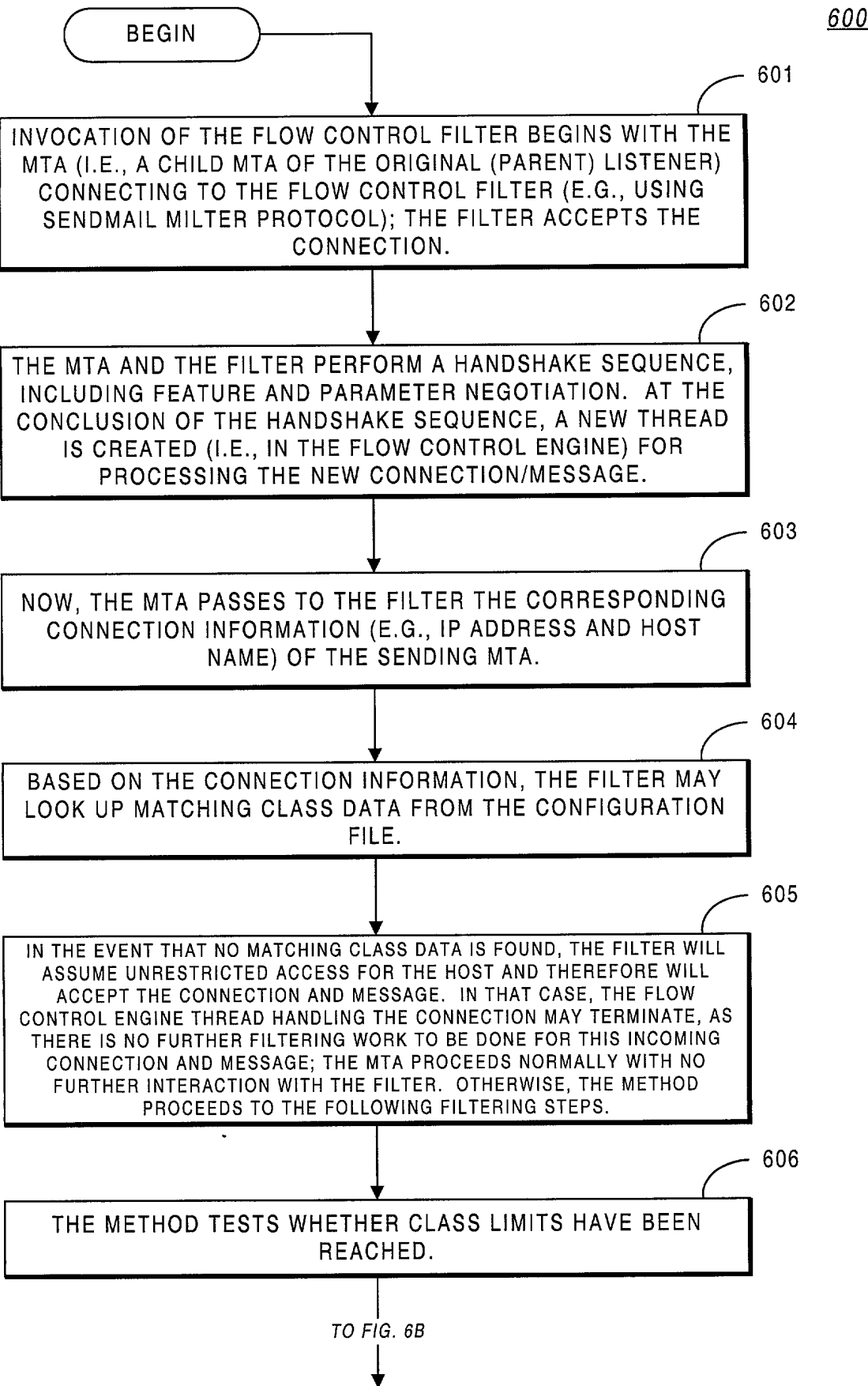


FIG. 6A

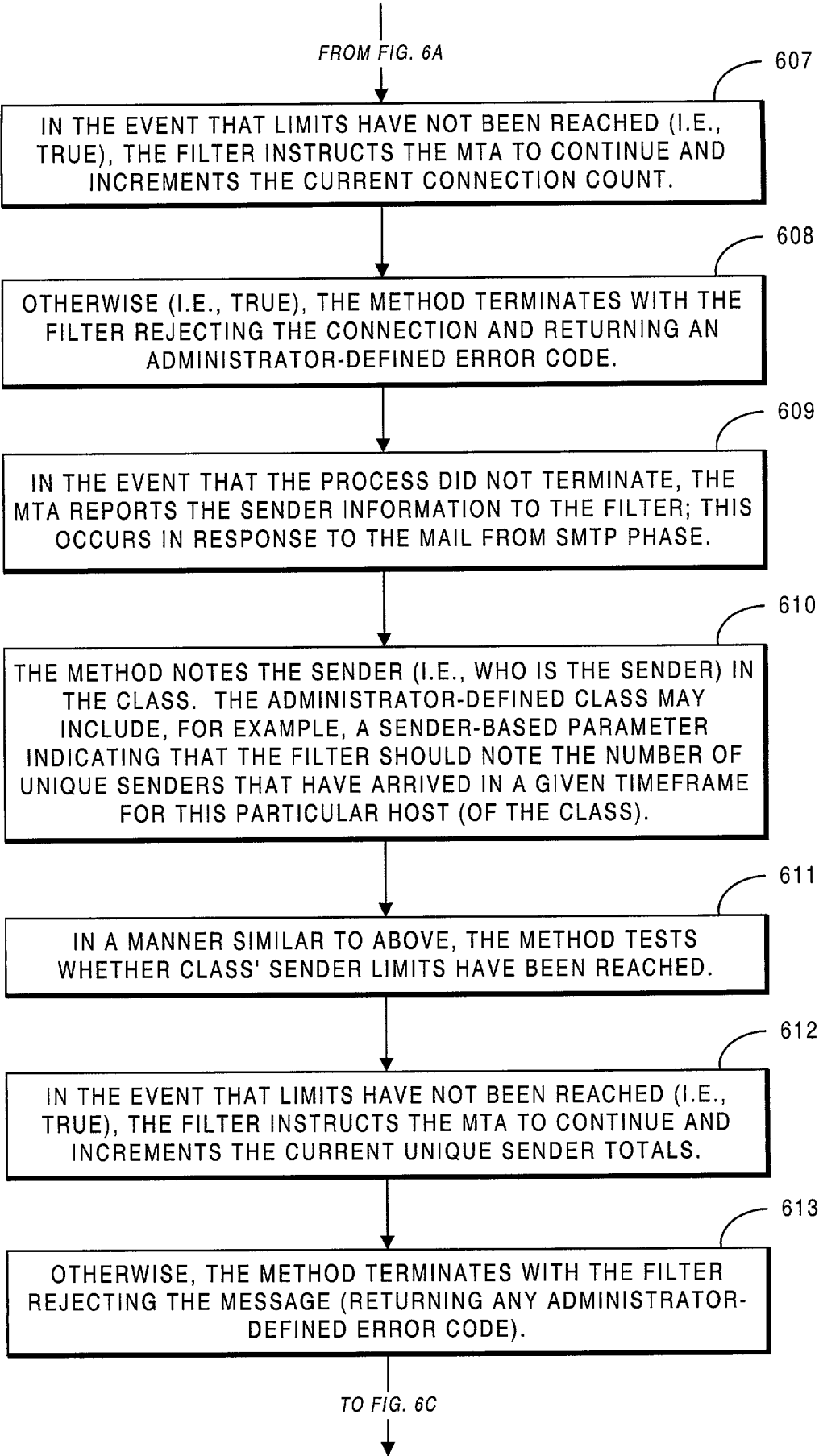


FIG. 6B

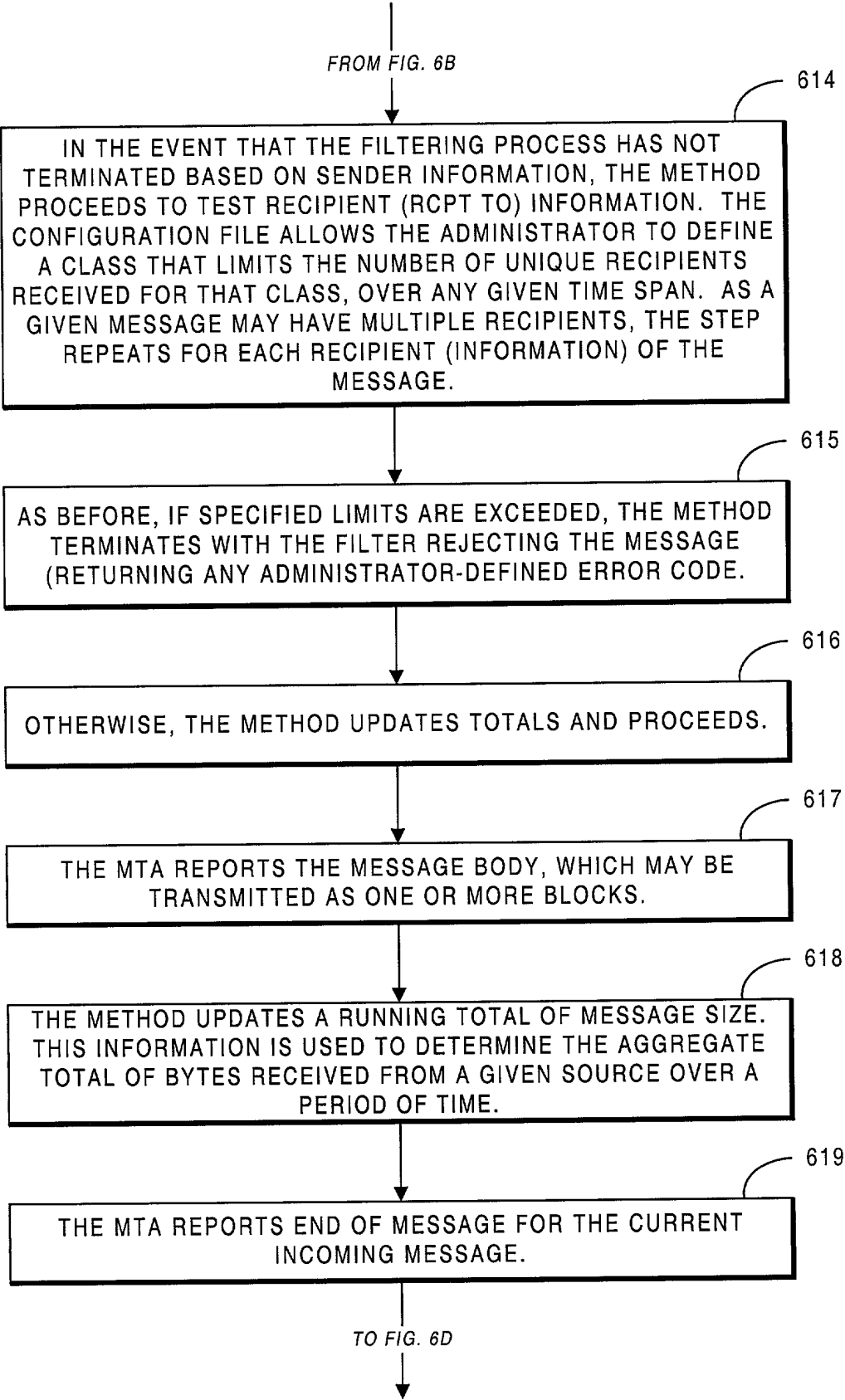


FIG. 6C

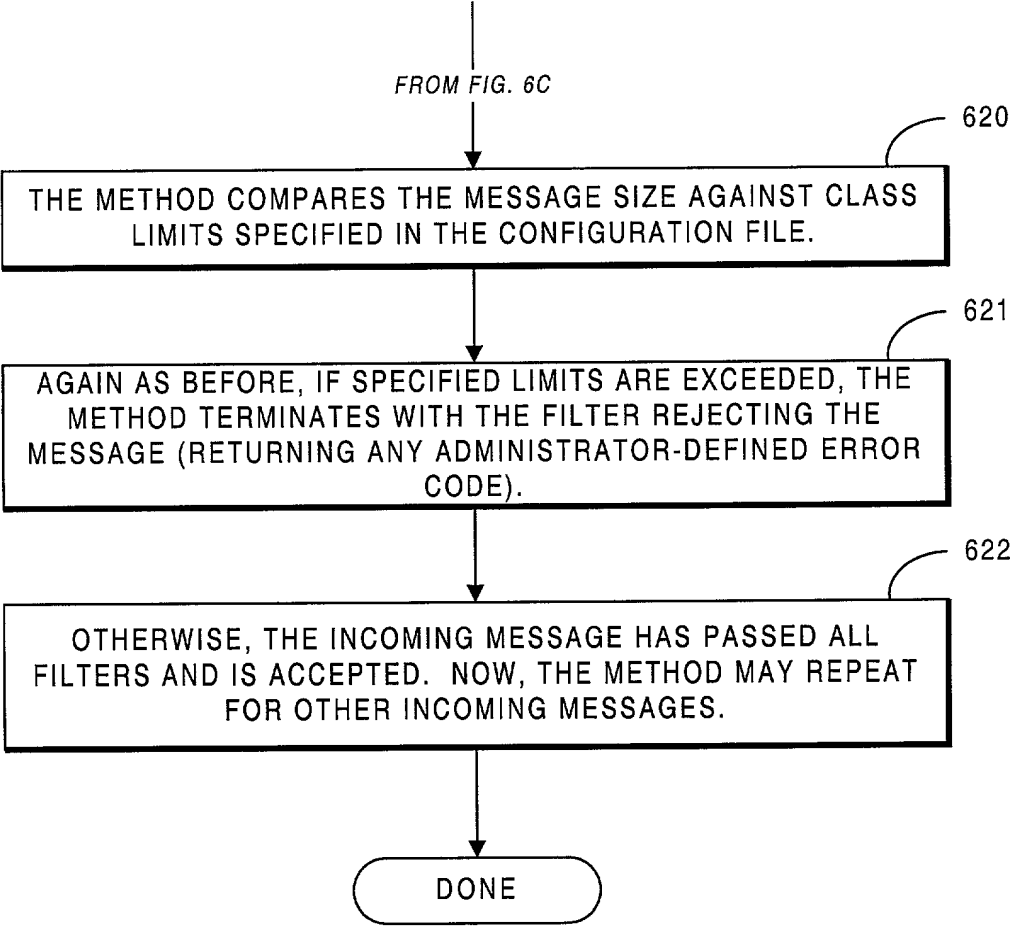


FIG. 6D